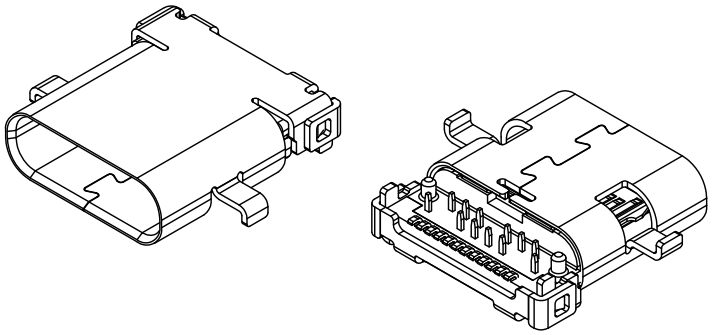
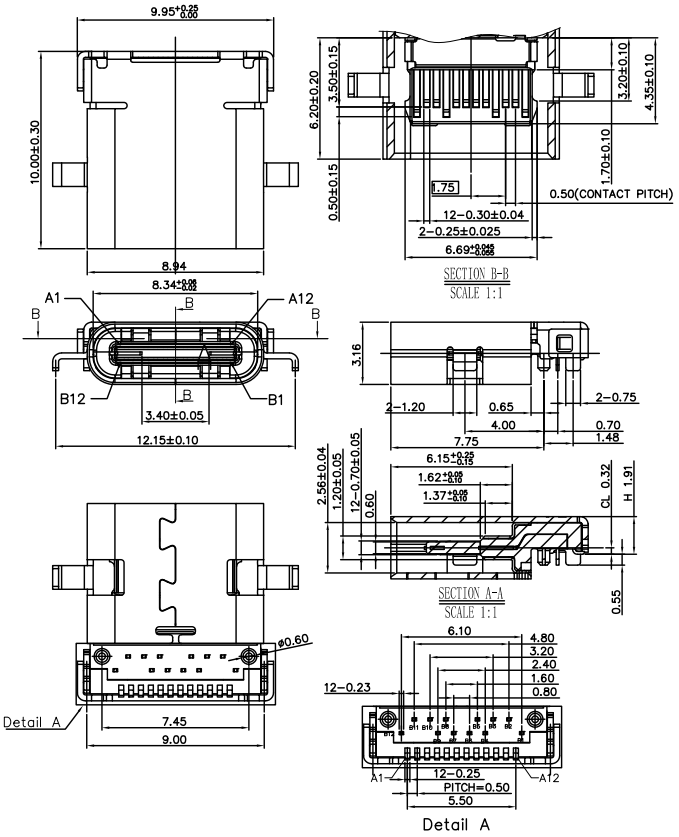
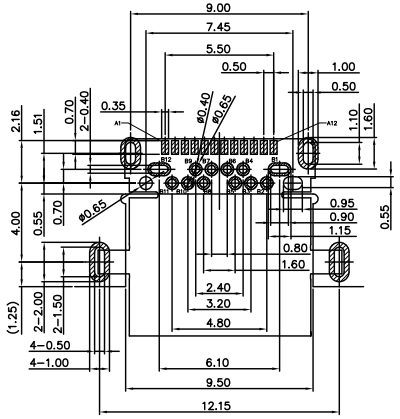
CUSTOMER DRAWING

NOTE:
1.OPEN/SHORT TEST:100%;
2.CONDUCTOR RESISTANCE:5Ω;
3.INSULATION RESISTANCE:20MΩ;
4.HI-POT TEST:300V/DC;
5.ALL MATERIALS MUST ACCORD WITH RoHS;

REVISIONS				
SYM	ECN No.	DESCRIPTION	DATE	APPROVED
A		NEW	04/27/2016	JOHNSON







RECOMMEND P.C.B LAYOUT(COMPONENT SIDE)

TOLERANCE FOR PCB LAYOUT IS ±0.05

KEEP OUT AREA

NOTES: 1.MATERIAL:
MOLDING: LCP UL94 V-0
CONTACT: COPPER ALLOY.
GOLD PLATED ON CONTACT AREA,100u"
Min TIN (LEAD FREE) ON SOLDER AREA,
SHELL: SUS304-H,T=0.30±0.03mm
50u" NICKEL PLATING OVER ALL.
SHILD: SUS304-H,T=0.12±0.03mm
2.MECHANICAL:
INSERTION: 5~20N.
EXTRACTION: 8~20N.
DURABILITY: 10000 CYCLES
3.ELECTRICAL:
CURRENT: 5A MIN FOR VBUS;
0.25A MIN FOR OTHER.
VOLTAGE: 5VAC MAX
WITHSTANDING VOLTAGE: 100V AC.
CONTACT RESISTANCE: 40mΩ MAX.
INSULATION RESISTANCE: 100MΩ MIN.
4.ENVIRONMENTAL
TEMPERATURE RANGE -55℃ ~ +85℃

USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTXp1	Positive half of firs SuperSpeed TX differential pair	B11	SSTXp1	Positive half of firs SuperSpeed TX differential pair
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSTXn1	Negative half of first SuperSpeed TX differential pair
A4	VBUS	Bus Power	B9	VBUS	Bus Power
A5	CC1	Configuration Channel	B8	CC1	Configuration Channel
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dp1	Positive half of the USB 2.0 differential pair-Position 1
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dn1	Negative half of the USB 2.0 differential pair-Position 1
A8	RFU1	Reserved for Future Use (RFU)	B5	RFU1	Reserved for Future Use (RFU)
A9	VBUS	Bus Power	B4	VBUS	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSRXn2	Negative half of second SuperSpeed RX differential pair
A11	SSRXn2	Positive half of second SuperSpeed RX differential pair	B2	SSRXn2	Positive half of second SuperSpeed RX differential pair
A12	GND	Ground return	B1	GND	Ground return

T50E7412 X XX X 0ET

CANTACT PLATING
0=G/F
1=2~3U
2= 5U
3=10u
4=15u
5=30u

SPECIAL OPTION
00=黑膠芯+ PCB (3A5V)+無線夾
01=黑膠芯+ PCB (3A5V)+帶線夾 (U2 28#+28#)
02=黑膠芯+ PCB (3A5V)+帶線夾 (U2 30#+28#+32#)
03=黑膠芯+ PCB (3A5V)+帶線夾 (U3.1 32#+32#+23#)
04=黑膠芯+ PCB (3A5V)+帶線夾 (U3.1 30#+34#+23#)

PACKING OPTION
0=TAPE & Reel Packing
1=Tube Packing
2=Tray paching

TOLERANCE UNIT(mm)		APPROVALS	DATE
X.	±0.30	DRAWN WINKI	06/20/2016
X.X	±0.25	CHECKED JOHNSON	06/20/2016
X.XX	±0.15	APPROVED JOHNSON	06/20/2016
X.XXX	±0.08		
ANGULAR	±2°		
UNLESS OTHERWISE SPECIFIED		UNIT MM	

TC&C Electronics Co.,Ltd

TITLE: USB 3.1 C FEMALE(雙排貼/24PIN)

SCALE N.A.	PART NO.	DRAWING NO.	REV.
SHEET 1 OF 2	T50E7412XXXX0ET	T50E7412XXXX0ET A	A

CUSTOMER CODE		CUSTOMER PART NO.	

CUSTOMER DRAWING

REVISIONS				
SYM	ECN No.	DESCRIPTION	DATE	APPROVED
A		NEW	04/27/2016	JOHNSON

NOTE:
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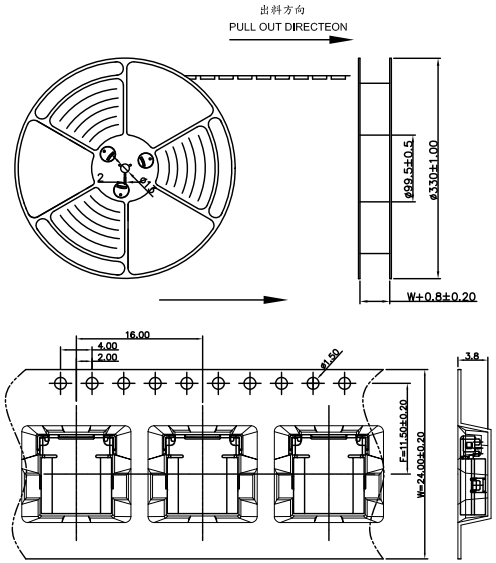
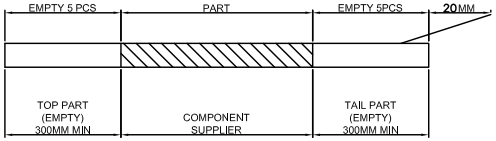
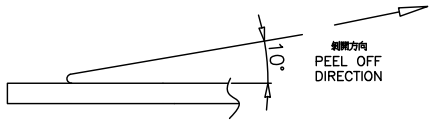


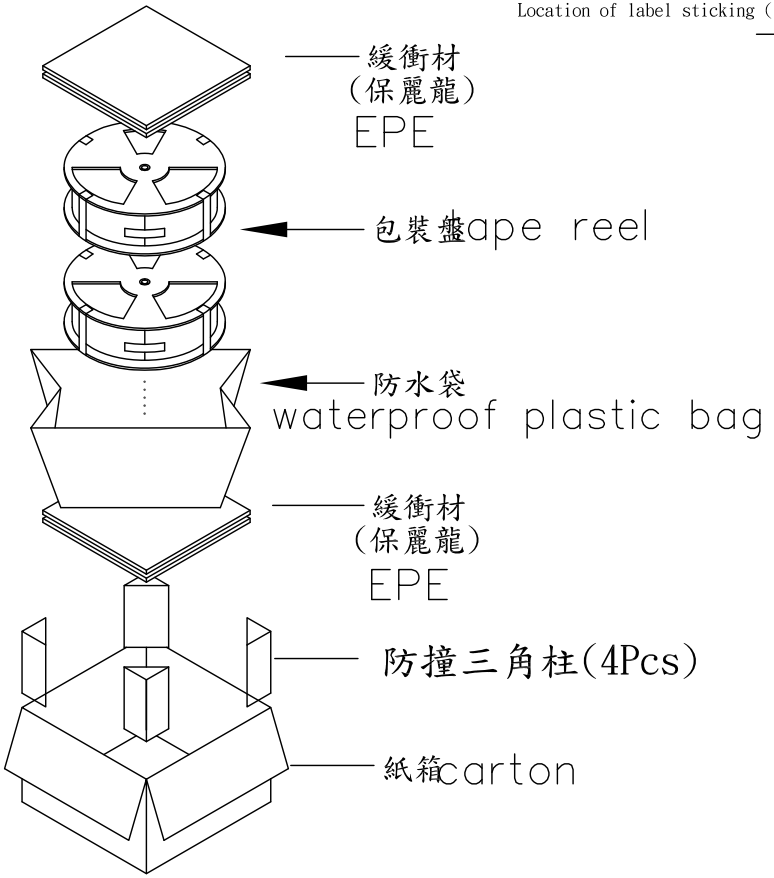
NOTES

1.帶子引導長度
LEAD TAPE LENGTH
出料方向
PULL OUT DIRECTEON

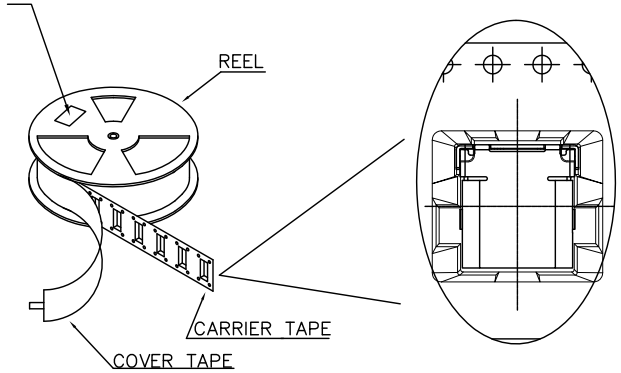


2.上帶 剝力力為：0.1~1.3**N(10.2gf~130gf)**
PEELING OFF FORCE OF TOP TAPE:0.1~1.3N(10.2gf~130gf)
出貨運輸過程中一定注意：
THIS REQUIREMENT SHOULD BE APPLIED AT SHIPMENT:
卷帶在運輸過程中不能敞開
PEELING OFF SHOULD NOT BE ALLOWED DURING TRANSPORTATION





at small positioning hole of the carrier tape side).
標簽黏貼處（朝捲裝包材的小定位孔一側）
Location of label sticking (label



NOTE:
1.每捲包裝數量及每箱裝箱數量如左下表格.
Packing q'ty of each reel & carton as below table shown.
2.以上膠帶覆蓋下包裝帶經包裝機熱熔包裝
Use cover tape to cover and hot melt machine to seal up.
3.每個紙箱放2個珍珠棉
Every carton use 2pcs EPE.
4.將裝滿產品的卷盤放入紙箱并以膠帶封口
Put reels in the carton and use adhesive tape to seal up.

TOLERANCE UNIT(mm)		APPROVALS	DATE
X.	±0.30	DRAWN WINKI	06/20/2016
X.X	±0.25	CHECKED JOHNSON	06/20/2016
X.XX	±0.15	APPROVED JOHNSON	06/20/2016
X.XXX	±0.08		
ANGULAR	±2°		
UNLESS OTHERWISE SPECIFIED		UNIT MM	

TC&C Electronics Co.,Ltd

TITLE
USB 3.1 C FEMALE(雙排貼/24PIN)

SCALE N.A.	PART NO.	DRAWING NO.	REV.
SHEET 2 OF 2	T50E7412XXXX0ET	T50E7412XXXX0ET A	A

CUSTOMER CODE		CUSTOMER PART NO.	